

IN THE CLAIMS

Please amend the claims as follows:

1-14. (cancelled)

15. (Currently Amended) A receiver comprising:

a trellis decoder ~~that is~~ configured to decode a first data stream and a second data stream, wherein

the trellis decoder decodes

the first data stream based on a first symbol map and corresponding first metric table, and

the second data stream based on a second symbol map and corresponding second metric table, wherein the second data map is configured to provide a higher gain to bits of the second data stream than the first data map provides to bits of the first data stream.

16. (Currently Amended) The receiver ~~of~~ as claimed in claim 15, wherein said receiver further ~~including~~ comprises:

a de-interleaver, operably coupled to the trellis decoder, ~~that reorders for reordering~~ bytes from the trellis decoder,

a Reed-Solomon decoder, operably coupled to the de-interleaver, ~~that corrects for correcting~~ errors among bytes from the de-interleaver, and

a de-randomizer, operably coupled to the Reed-Solomon decoder, ~~that reorders for reordering~~ data from the Reed-Solomon

decoder to provide packets corresponding to the first data stream and second data stream.

17. (Currently Amended) The receiver ~~of~~ as claimed in claim 16,
wherein said receiver further including comprises:

a multiplexer, operably coupled to the trellis decoder,
that is configured to order the bytes of the first and second data
stream for processing by the de-interleaver,

wherein the multiplexer receives a control input ~~that~~
~~controls~~ for controlling a selection of bytes corresponding to the
first data stream or the second data stream.

18. (Currently Amended) The receiver ~~of~~ as claimed in claim 16,
wherein the receiver is configured to decode at least the first
data stream in conformance with ATSC standards for the Vestigial
Side Band subsystem of the Digital Transmission Standard for the
transmission of digital television signals.

19. (Currently Amended) The receiver ~~of~~ as claimed in claim 15,
wherein said receiver further including comprises:

a ~~post~~ post-processor ~~that for further decodes decoding~~
the second data stream via a subsequent error correcting process.

20. (Currently Amended) The receiver ~~of~~ as claimed in claim 19,
wherein the ~~post~~ post-processor is enabled in dependence upon a
control parameter in an MPEG header.

21. (Currently Amended) The receiver ~~of as claimed in~~ claim 15, wherein the second data map is configured to minimize the effects of a symbol error.

22. (Currently Amended) The receiver ~~of as claimed in~~ claim 15, wherein redundant encoded bits are encoded by applying a maximum mapping gain and non-redundantly encoded bits are encoded by applying a minimum mapping gain.

23. (Currently Amended) A receiver for selectively decoding a data stream in a first or a second decoding mode, the receiver comprising:

a trellis decoder ~~that is~~ configured to:

in the first decoding mode, decode a received symbol based on a first metric table corresponding to the inverse of a first map that provides the symbol mapping of the first mode; and

in the second decoding mode, decode a received symbol based on a second metric table corresponding to the inverse of a second map that provides the symbol mapping of the second mode, wherein

the second data map is configured to provide a higher gain than the first data map provides to bits of the first data stream.

24. (Currently Amended) The receiver ~~of~~as claimed in claim 23, wherein the second data map is further configured to minimize the effects of a symbol error.